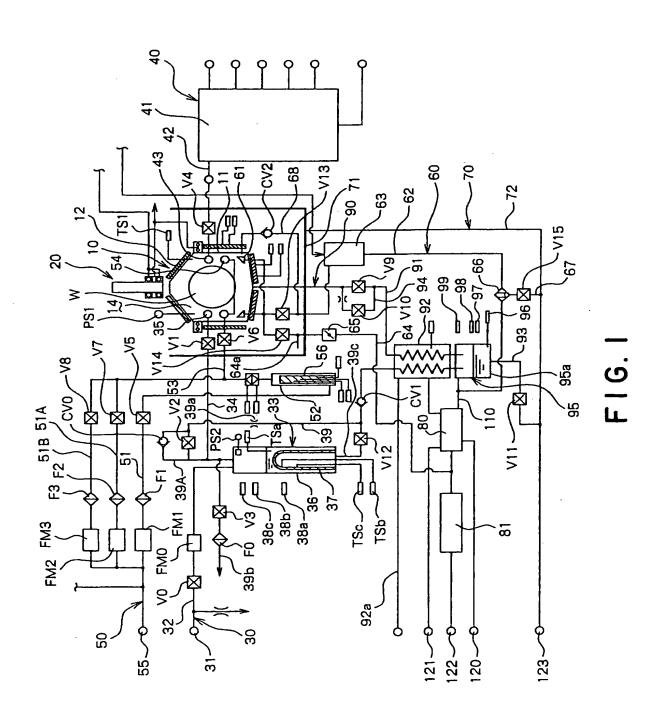
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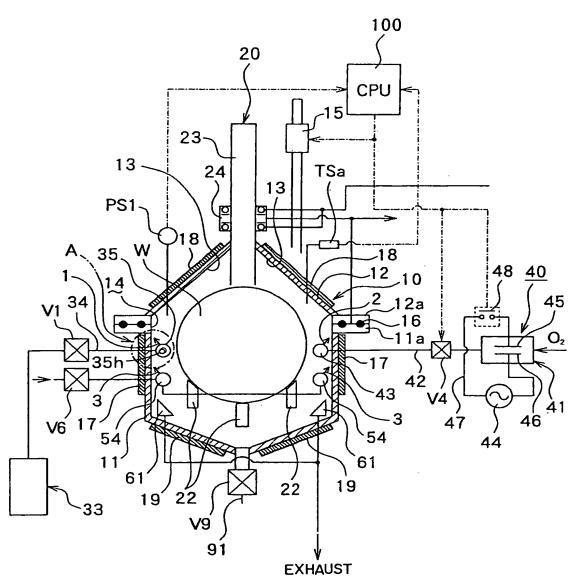


FIG. 2A

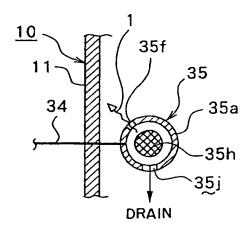
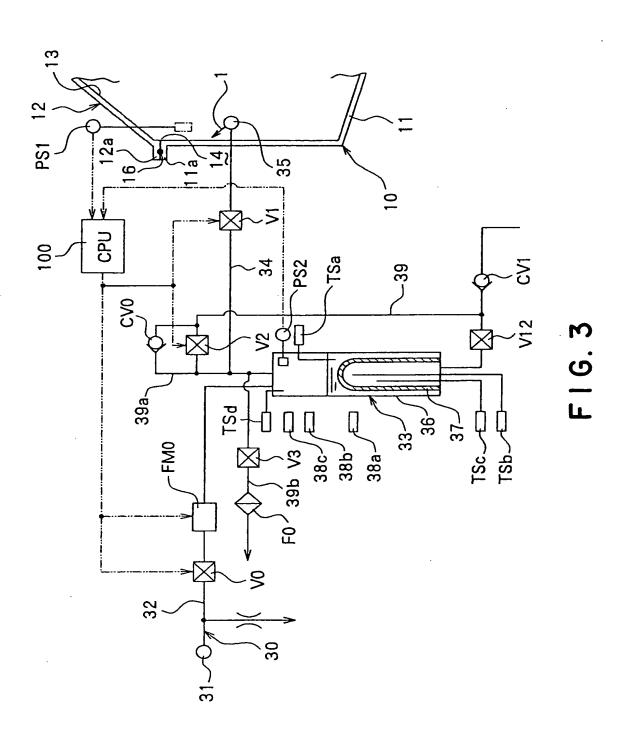


FIG. 2B

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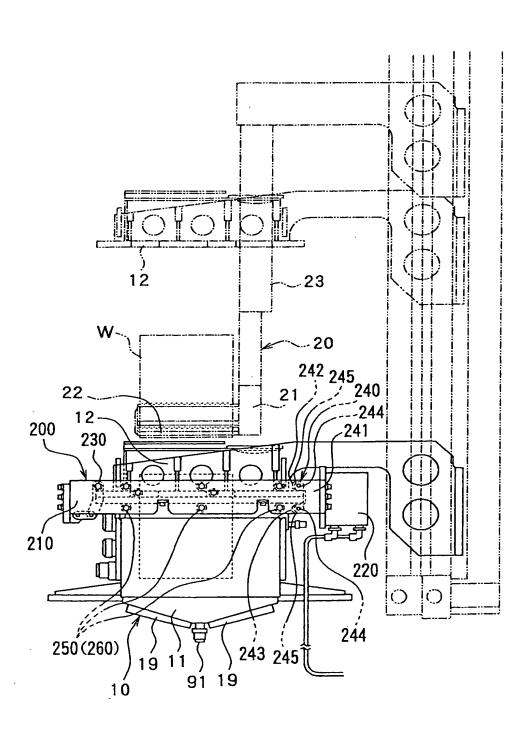
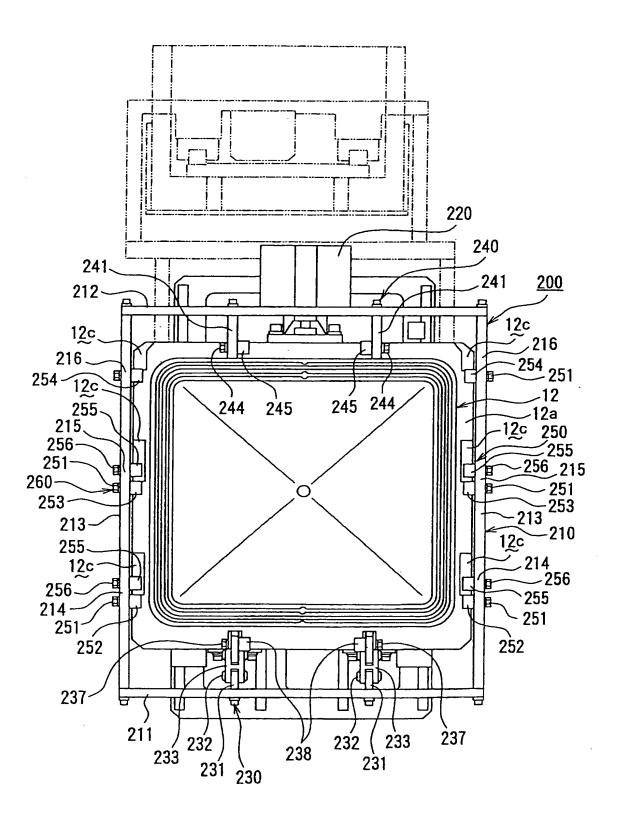


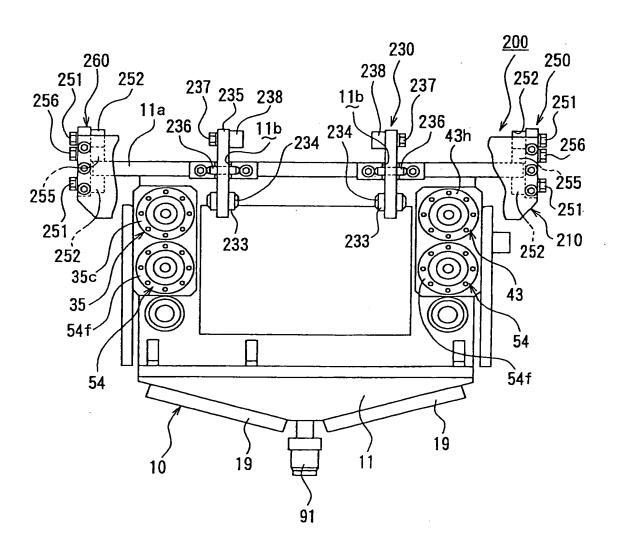
FIG. 4

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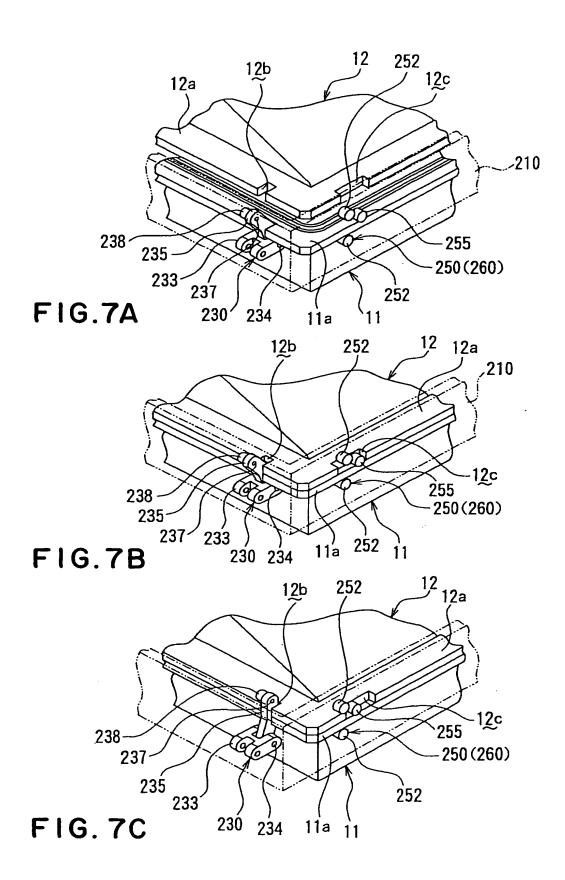
F I G. 5

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F I G. 6

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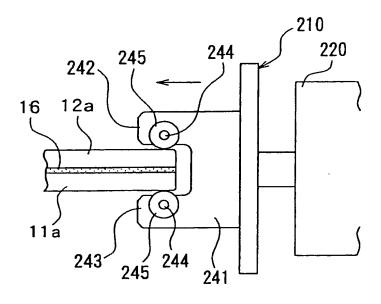


FIG. 8A

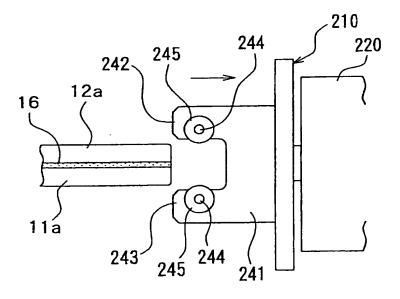


FIG. 8B

Title: Substrate Processing Apparatus and Substrate Processing Method Inventor(s): Naoki SHINDO, et al.

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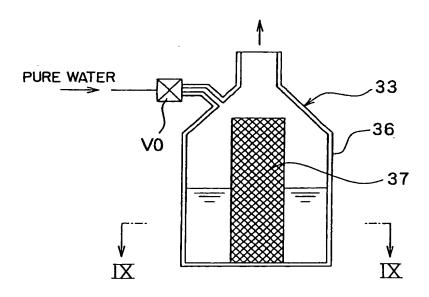


FIG. 9A

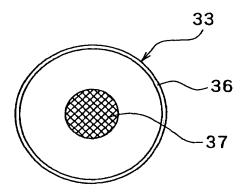


FIG. 9B

Inventor(s): Naoki SHINDO, et al. Application No.: Not yet assigned Docket No.: 199372003910

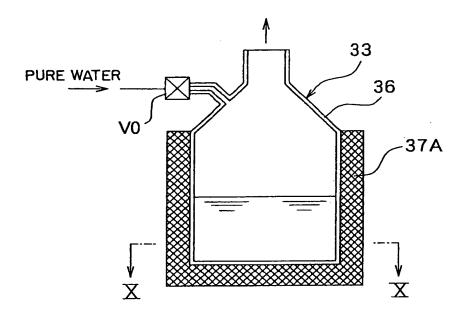


FIG. IOA

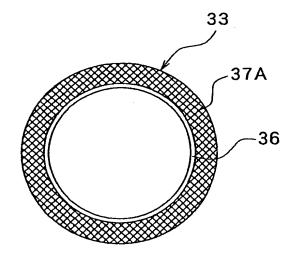


FIG. IOB

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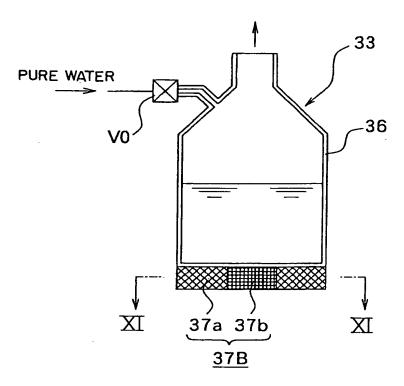


FIG. IIA

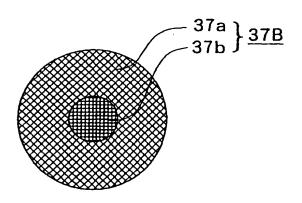
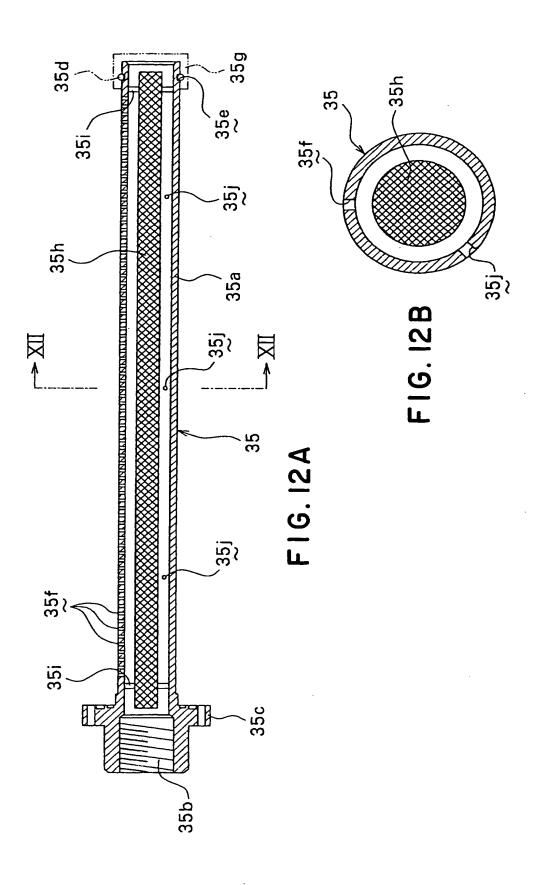
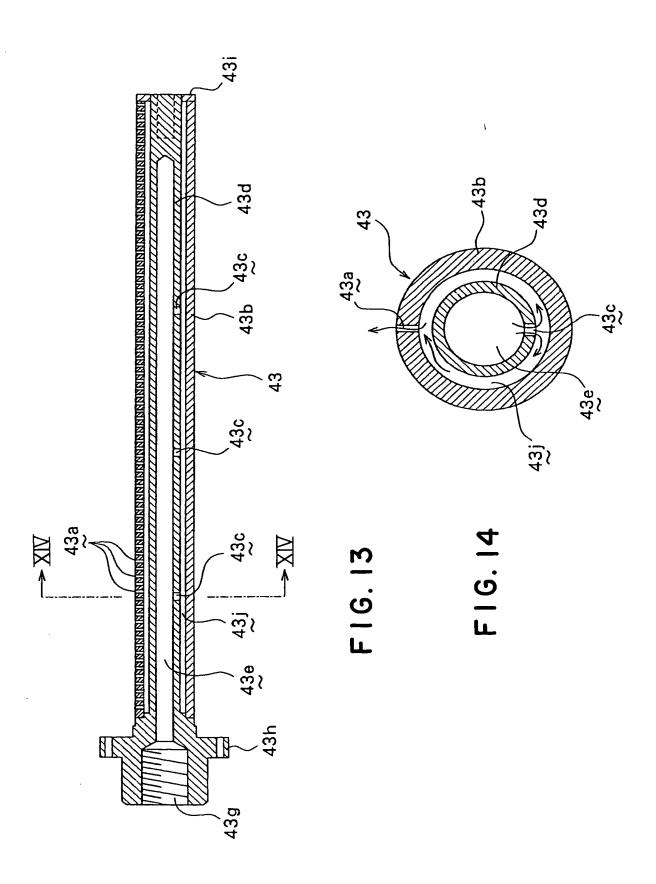
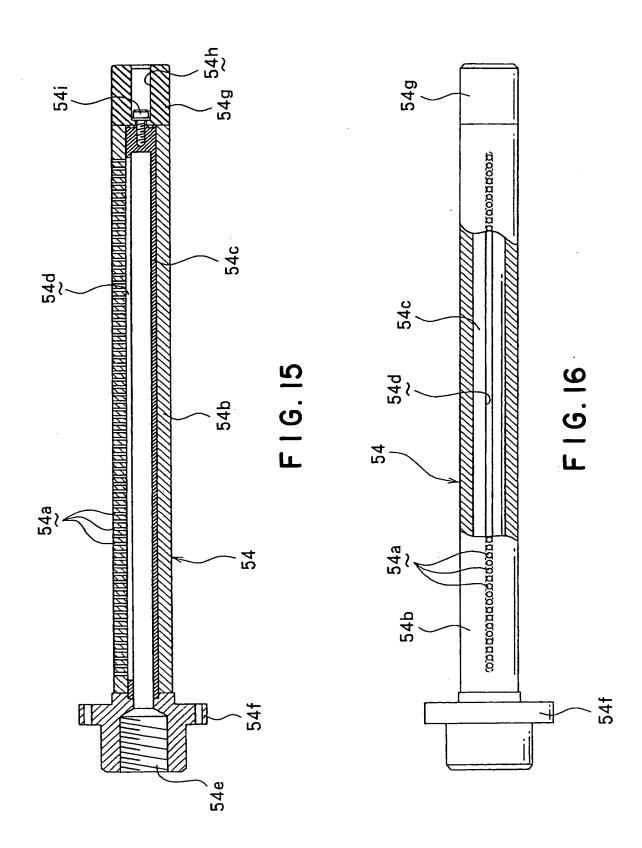


FIG. IIB

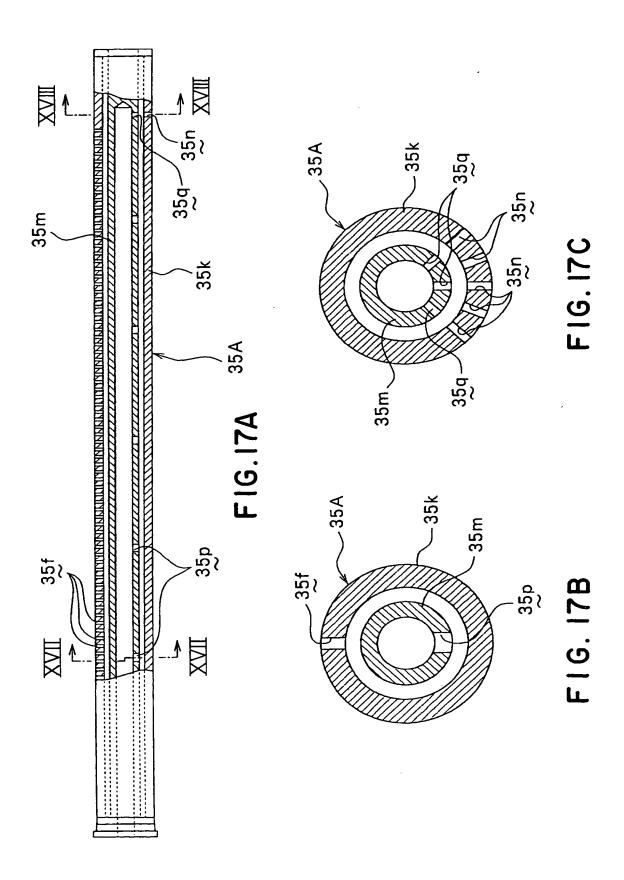


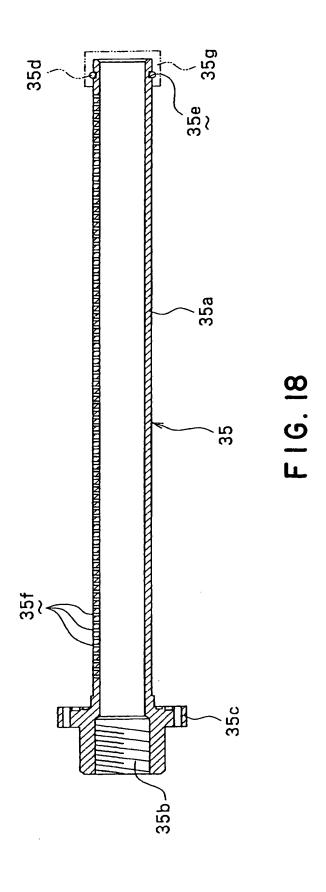




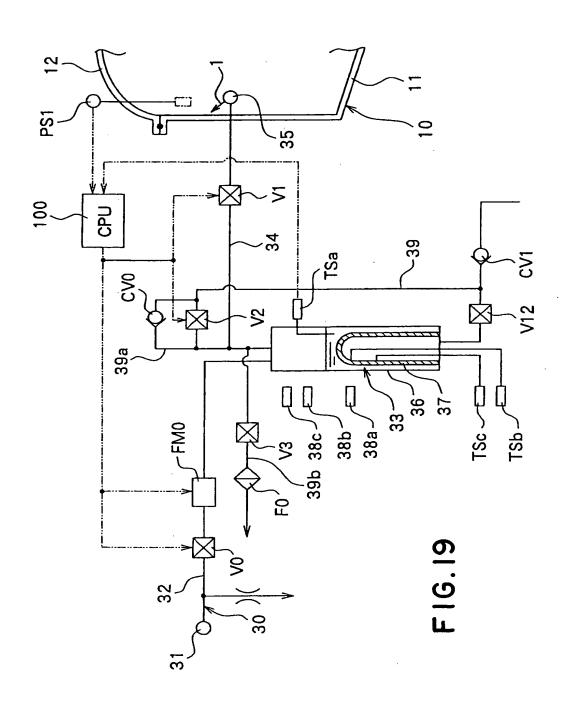
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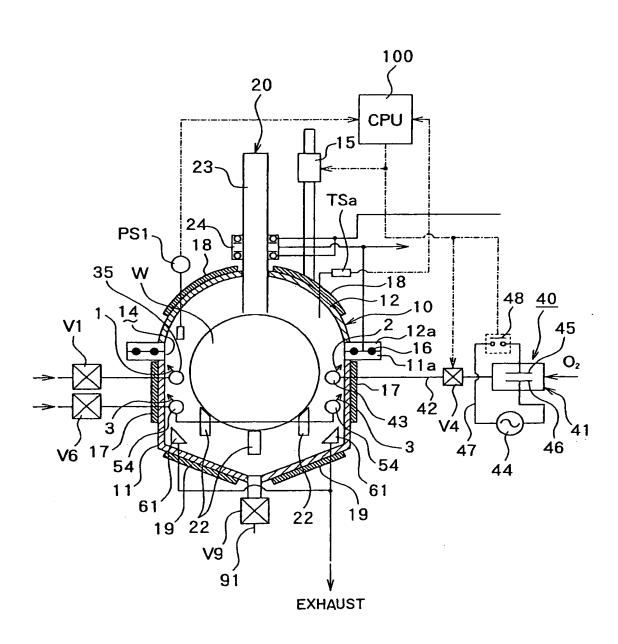


FIG. 20